

PATENT APPLICATION

Docket No.: 4591-306

Serial No. not yet assigned

Applicant: Jae-Yoon Sim

Filing Date: June 30, 2003

Group: not yet assigned

**INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)**

U.S. PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub</u> <u>Class</u>
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FOREIGN PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
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OTHER DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
015		Article from IEEE Journal of Solid-State Circuits, Vol. 29 No. 1, pp. 9-13 January 1994, entitled "OFFSET COMPENSATING BIT-LINE SENSING SCHEME FOR HIGH DENSITY DRAM'S"

Examiner: _____

M. TRAN

Date Considered: _____

6/22/04